

Abstract of the Disclosure

A device for driving an LCD includes a timing control unit, a gate driving unit having a shift register and an output circuit, and a control signal transmission line for transmitting a data carry signal for enabling the shift register and a signal for controlling an data output by the output circuit using a single signal line. The data carry signal uses a rising edge trigger system, and the output control signal uses a level trigger system. In order to prevent an overlapping of the data carry signal and the output control signal, the output control signal is outputted after one clock from a time point where the data carry signal is latched using the shift register.

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